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AMENDMENT TRANSMITTAL LETTER (Large Entity) Applicant(s): Kiyohito MUKAI, et al.						Docket No. L8462.03118		
Application No. 10/715,119	Filing Date November 18, 2003	Examiner N. M. Doan		Customer No 52989	0.	Group Art Unit 2825	Confirmation No. 2755	
SEMICONDUCTOR DEVICE LAYOUT INSPECTION METHOD								
T& TRADEMAND		COMMISSIONER FO	OR PATI	ENTS:				
Transmitted herewith is an amendment in the above-identified application. The fee has been calculated and is transmitted as shown below.								
		CLAIMS AS AN	MENDE)	_			
	CLAIMS REMAINING	HIGHEST #	NUMB	ER EXTRA		RATE	ADDITIONAL	
	AFTER AMENDMENT	PREV. PAID FOR	CLAIMS	S PRESENT			FEE	
TOTAL CLAIMS	4 -	20 =			Х	\$50.00	\$0.00	
INDEP. CLAIMS	1 -	12 =		0 ;	X	\$200.00	\$0.00	
Multiple Dependent	t Claims (check if appl	olicable)					\$0.00	
I		TOTAL ADDITIONAL	FEE FO	R THIS AME	ND	MENT	\$0.00	
No additional fee is required for amendment. Please charge Deposit Account No. in the amount of A check in the amount of to cover the filing fee is enclosed. The Director is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account 19-4375 Any additional filing fees required under 37 C.F.R. 1.16. Any patent application processing fees under 37 CFR 1.17. Payment by credit card. Form PTO-2038. WARNING: Information on this form may become public. Credit card information should not be included on this form. Provide credit card information and authorization on PTO-2038. Dated: June 19, 2006 I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on								
(Date) Signature of Person Mailing Correspondence							espondence	
cc:				Typed or Printed Name of Person Mailing Correspondence				



In re the Application of

Inventor: Kiyohito MUKAI et al. Group Art Unit: 2825

Appln. No.: 10/715,119 Examiner: N. M. Doan

Filed: November 18, 2003

For: SEMICONDUCTOR DEVICE LAYOUT INSPECTION METHOD

AMENDMENT UNDER 37 CFR § 1.116

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated April 18, 2006, please amend the above-captioned application as follows: